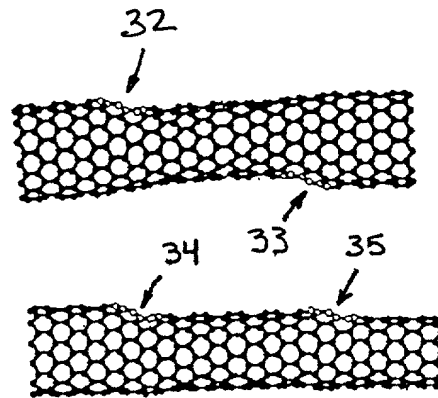


Figure 1



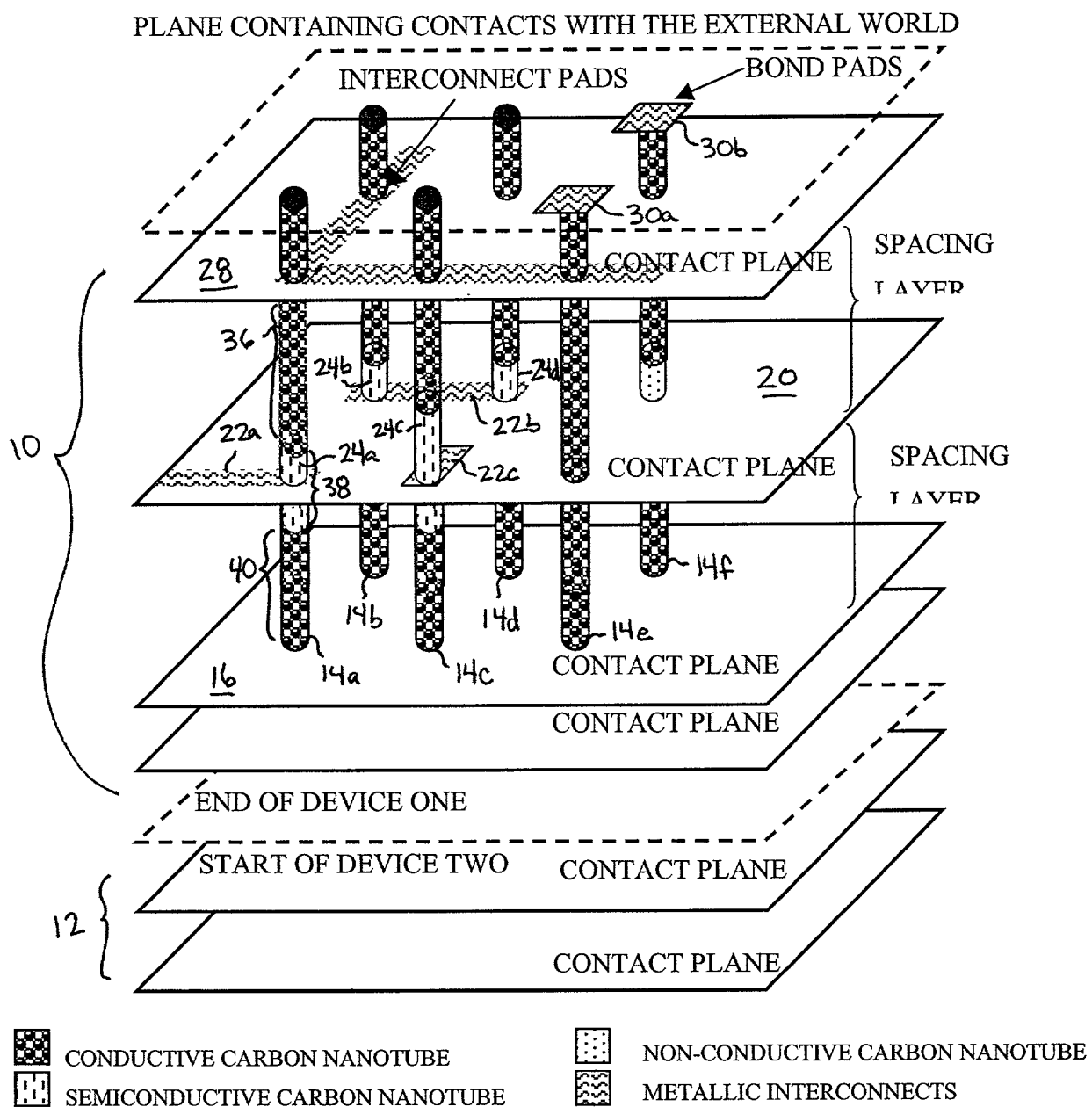


Figure 2

## FABRICATION/SYNTHESIS PROCESSES AND DESIGN ARCHITECTURE

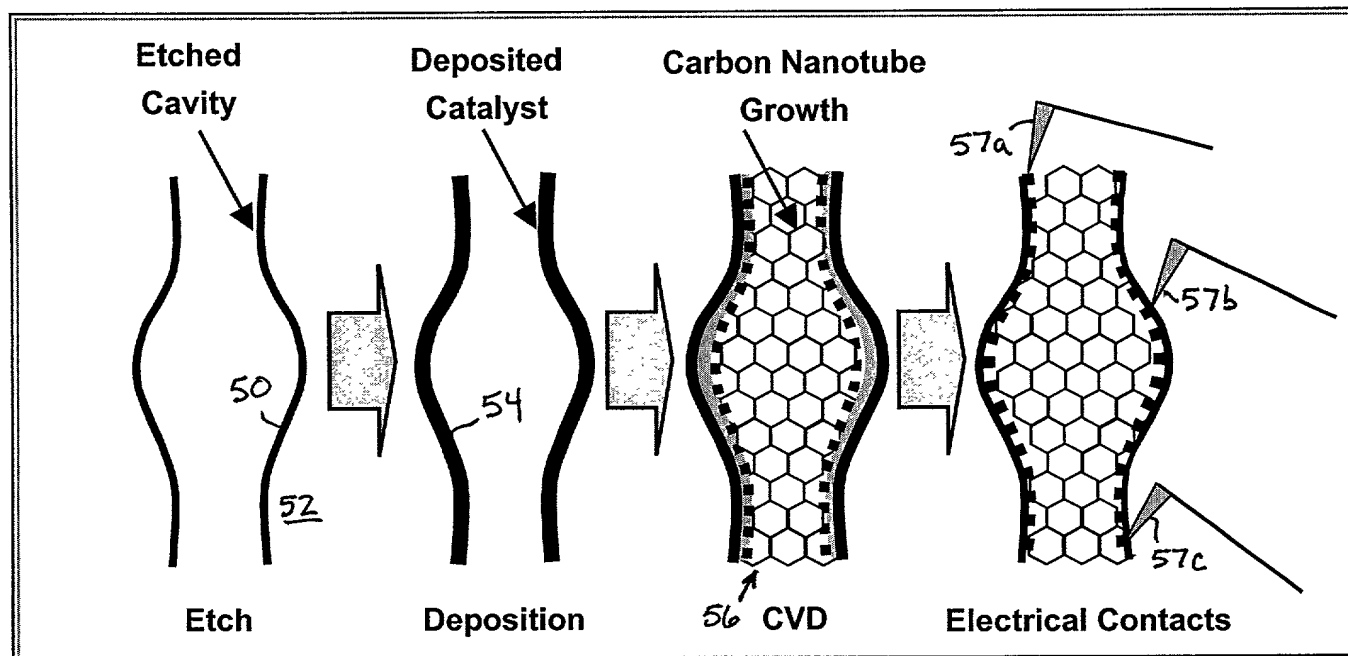
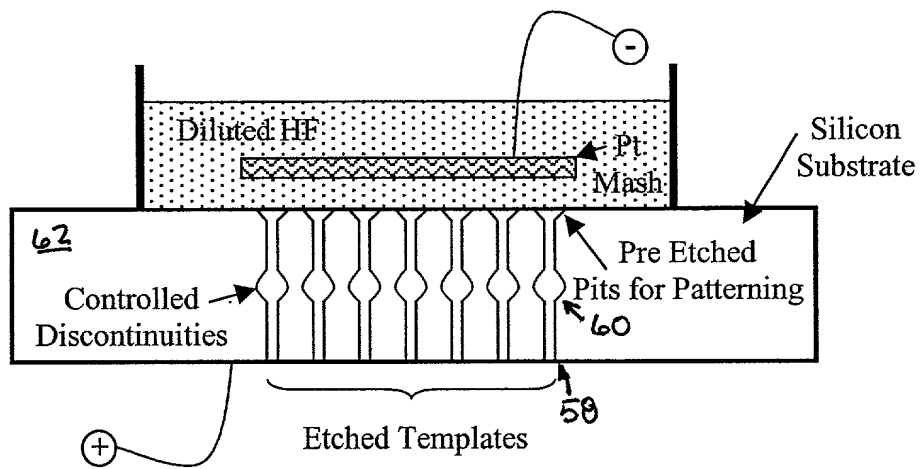


Figure 3



**Figure 4**

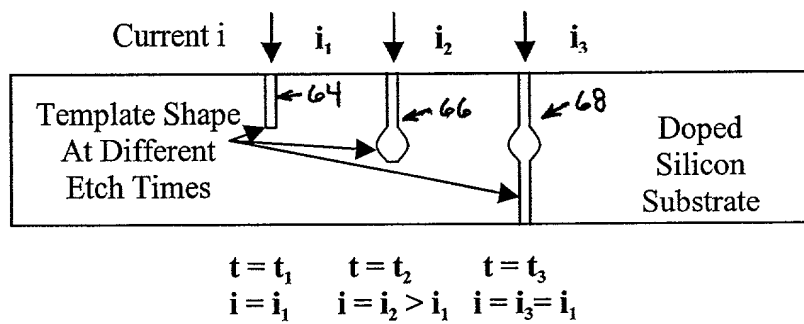
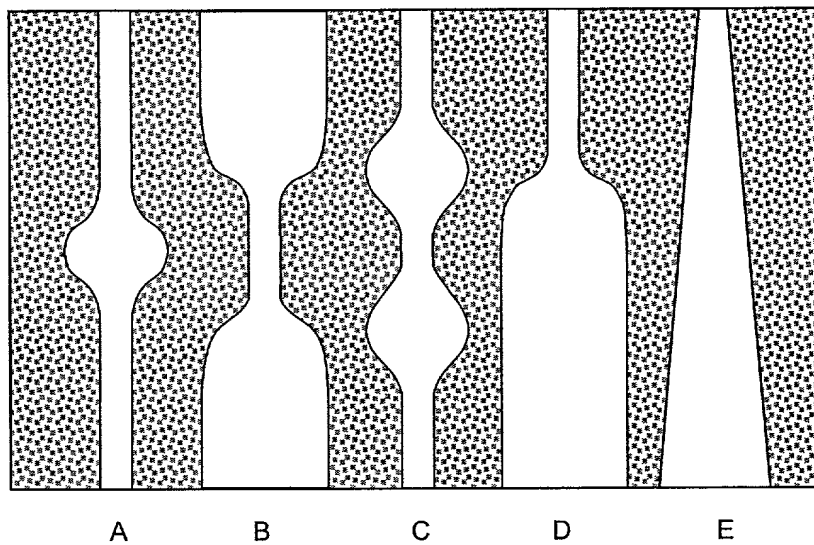
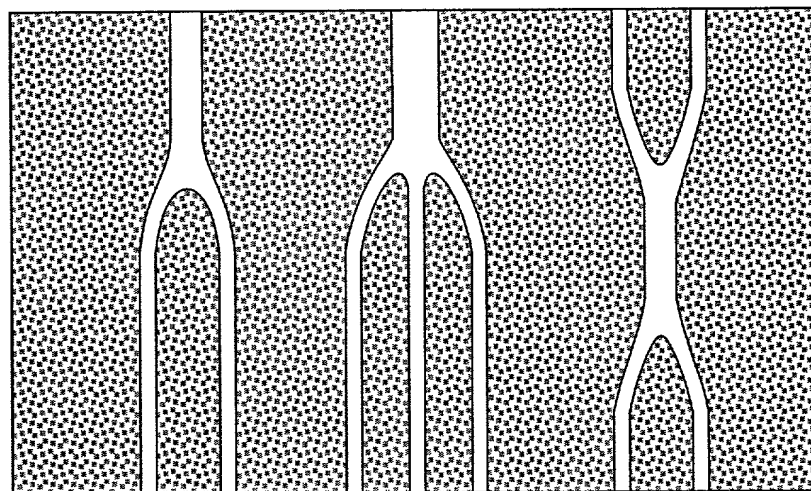


Figure 5



**Figure 6**

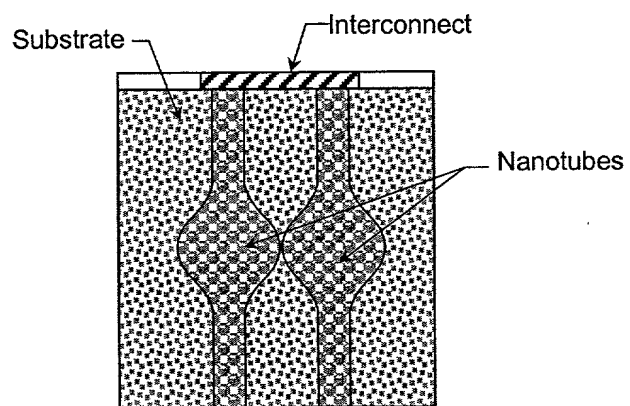


A

B

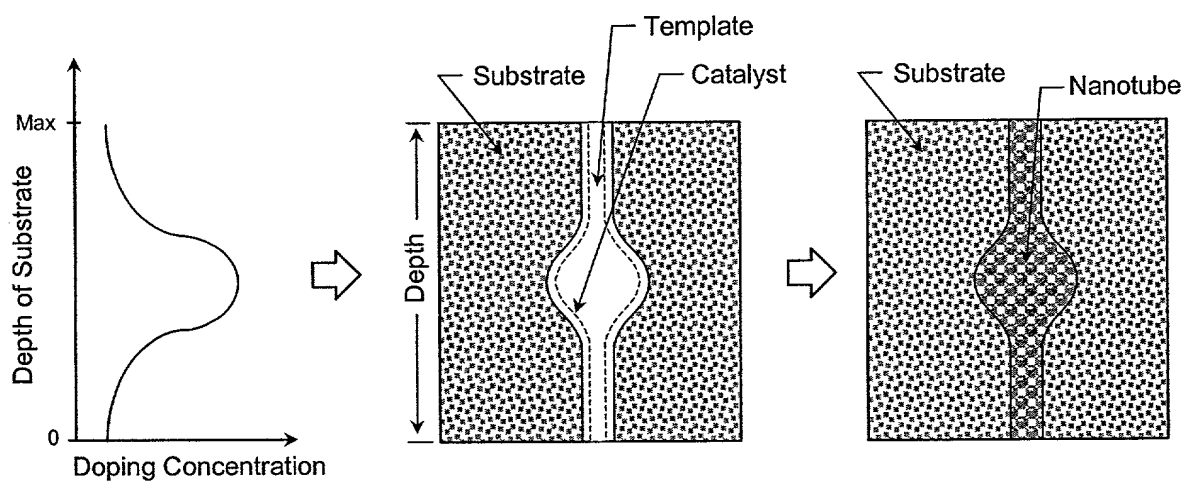
C

**Figure 7**

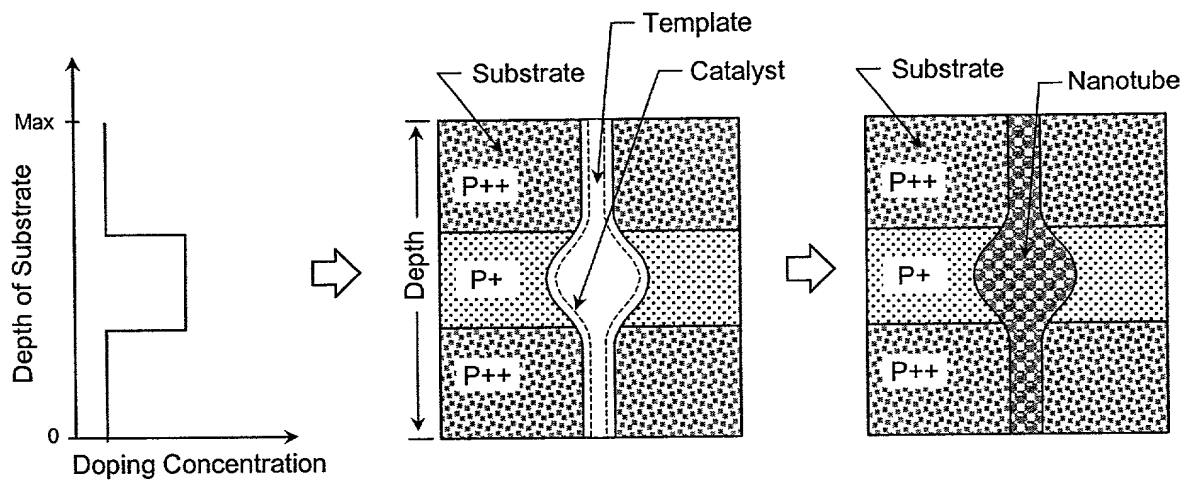


**Figure 8**

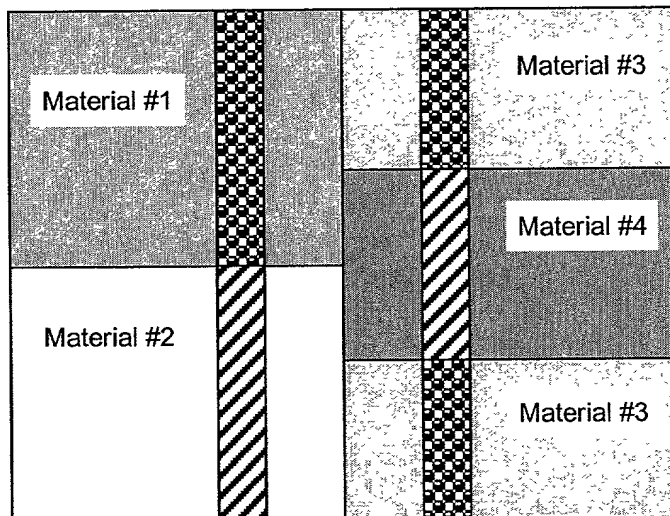




**Figure 9**



**Figure 10**



**Figure 11**

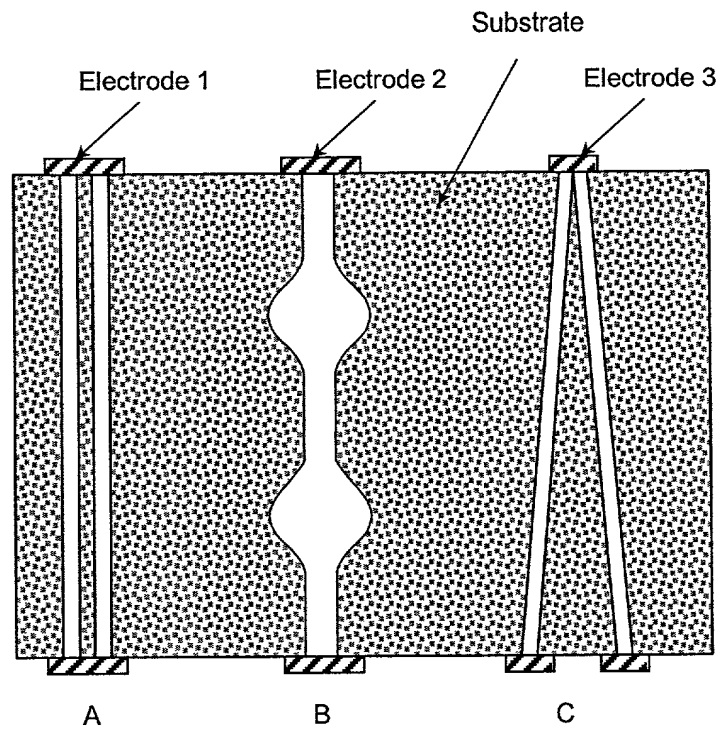


Figure 12

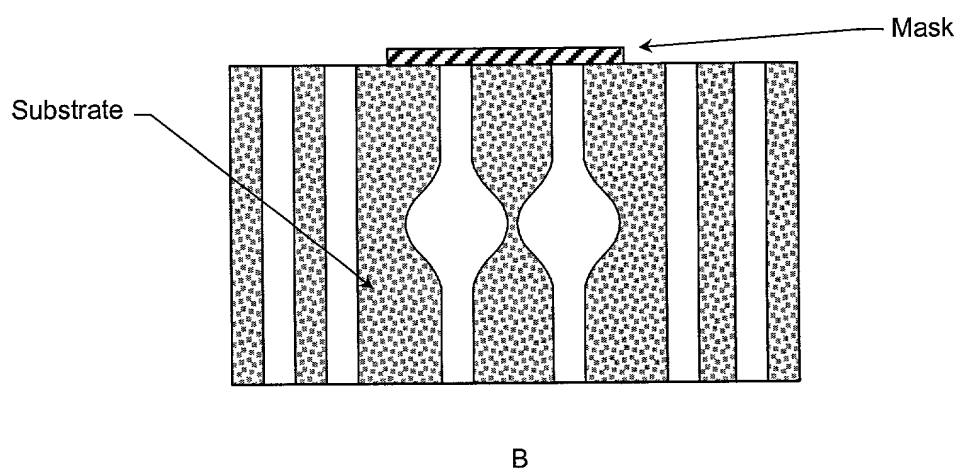
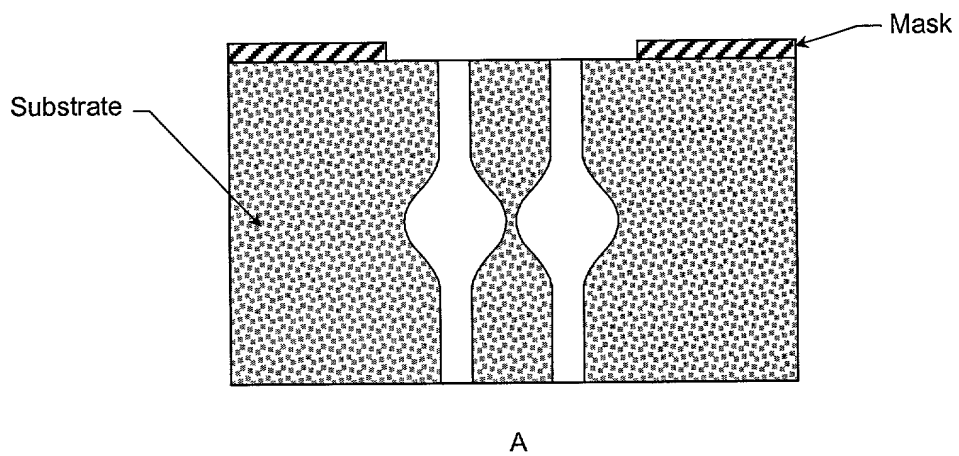


Figure 13